

Wide-Bandwidth 4-Channel SPDT Video Analog Switch

1 FEATURES

- **Wide Bandwidth: 220MHz**
- **Single Supply Operation +1.8V to +5.5V**
- **Low ON Resistance, 8Ω(TYP)**
- **Low Crosstalk: -60dB at 10MHz (TYP)**
- **Rail-to-Rail Operation**
- **Fast Switching Time**
- **Operating Temperature Range: -40°C to 125°C**
- **ESD Protection Exceeds JESD 22**
 - **6000-V Human-Body Model**
 - **400-V Machine Model (A115)**
 - **1000-V Charged-Device Model (JS-002)**
- **PACKAGES: SOP16, SSOP16, TSSOP16**

2 APPLICATIONS

- **Game Consoles**
- **Audio and Video Switching**
- **DVD Players**
- **Personal Video Recorders**
- **Supports defense, aerospace and medical applications**
- **Desktop Video Editors**
- **Hard Disk Recorders**

3 DESCRIPTIONS

The RS2233 is a CMOS analog IC configured as a quad, bidirectional, single-pole/double-throw (SPDT) switches. This CMOS device can operate from 1.8 V to 5.5 V.

The select (IN) input control the data flow. The FET multiplexers/demultiplexers are disabled when the output-enable (\overline{OE}) input is high.

The device are digitally-controlled analog switches. It has low on-resistance (8Ω TYP) and low crosstalk (-60dB at 10MHz TYP).

The RS2233 is available in Green SOP16, SSOP16, TSSOP16 packages. It operates over an ambient temperature range of -40°C to 125°C.

Device Information⁽¹⁾

| PART NUMBER | PACKAGE | BODY SIZE (NOM) |
|-------------|---------|-----------------|
| RS2233 | SOP16 | 9.90mm×3.91mm |
| | SSOP16 | 4.90mm×3.90mm |
| | TSSOP16 | 5.00mm×4.40mm |

(1) For all available packages, see the orderable addendum at the end of the data sheet.

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4 REVISION HISTORY

Note: Page numbers for previous revisions may different from page numbers in the current version.

| Version | Change Date | Change Item |
|---------|-------------|---|
| A.1 | 2021/08/06 | Initial version completed |
| A.2 | 2021/12/09 | 1. Change the Minimum operating voltage to 1.8V 2. Add the Junction temperature |
| A.3 | 2023/08/14 | 1. Add ΔR_{ON} and $R_{FLAT(ON)}$ PARAMETER 2. Add Typical Ron as a Function of Input Voltage curve in 9 TYPICAL CHARACTERISTICS |
| A.4 | 2024/01/19 | 1. Update FEATURES on Page 1@RevA.3 1. Add MSL 2. Update Package thermal impedance and ESD Ratings on Page 7@RevA.3 |
| A.4.1 | 2024/03/07 | Modify packaging naming |
| A.5 | 2025/11/13 | Update Operating Temperature Range: -40°C to 125°C |

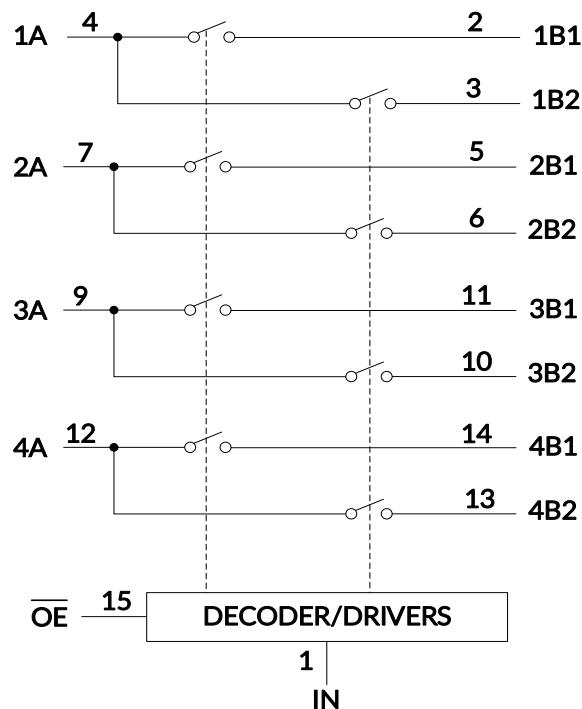
5 PACKAGE/ORDERING INFORMATION ⁽¹⁾

| PRODUCT | ORDERING NUMBER | TEMPERATURE RANGE | PACKAGE LEAD | PACKAGE MARKING ⁽²⁾ | MSL ⁽³⁾ | PACKAGE OPTION |
|---------|-----------------|-------------------|--------------|--------------------------------|--------------------|--------------------|
| RS2233 | RS2233YS16 | -40°C~125°C | SOP16 | RS2233 | MSL3 | Tape and Reel,4000 |
| | RS2233YSS16 | -40°C~125°C | SSOP16 | RS2233 | MSL3 | Tape and Reel,4000 |
| | RS2233YTSS16 | -40°C~125°C | TSSOP16 | RS2233 | MSL3 | Tape and Reel,4000 |

NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.
- (3) RUNIC classify the MSL level with using the common preconditioning setting in our assembly factory conforming to the JEDEC industrial standard J-STD-20F, Please align with RUNIC if your end application is quite critical to the preconditioning setting or if you have special requirement.

6 PIN CONFIGURATION AND FUNCTION



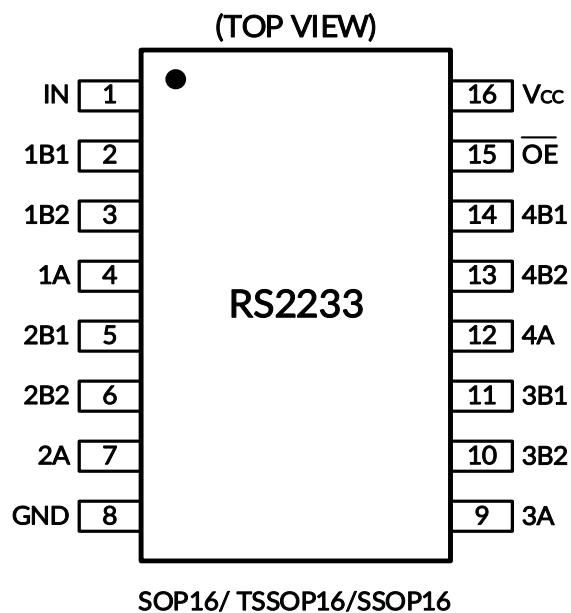
FUNCTION TABLE

| INPUTS | | FUNCTION |
|-----------------|----|-----------------|
| \overline{OE} | IN | |
| L | L | A port =B1 port |
| L | H | A port =B2 port |
| H | X | Disconnect |

X=Don't care

NOTE: Input and output pins are identical and inter-changeable. Either may be considered an input or output; signals pass equally well in either direction.

7 PIN CONFIGURATIONS



PIN DESCRIPTION

| NAME | PIN | FUNCTION |
|-----------------|----------------------|----------------------|
| | SOP16/TSSOP16/SSOP16 | |
| IN | 1 | Select Input. |
| 1B1 | 2 | Analog Video I/O. |
| 1B2 | 3 | Analog Video I/O. |
| 1A | 4 | Analog Video I/O. |
| 2B1 | 5 | Analog Video I/O. |
| 2B2 | 6 | Analog Video I/O. |
| 2A | 7 | Analog Video I/O. |
| GND | 8 | Ground. |
| 3A | 9 | Analog Video I/O. |
| 3B2 | 10 | Analog Video I/O. |
| 3B1 | 11 | Analog Video I/O. |
| 4A | 12 | Analog Video I/O. |
| 4B2 | 13 | Analog Video I/O. |
| 4B1 | 14 | Analog Video I/O. |
| \overline{OE} | 15 | Switch-Enable Input. |
| V _{cc} | 16 | Power Supply. |

8 SPECIFICATIONS

8.1 Absolute Maximum Ratings ⁽¹⁾

Over operating free-air temperature range (unless otherwise noted) ⁽¹⁾

| SYMBOL | PARAMETER | | MIN | MAX | UNIT |
|---------------|--|--------------|------|--------------|---------------|
| V_{CC} | Supply Voltage | | -0.3 | 6 | |
| V_{IN} | Input Voltage (All inputs) | | -0.3 | $V_{CC}+0.3$ | V |
| I_{IK} | Input clamp current | $V_{IO} < 0$ | | -50 | mA |
| θ_{JA} | Package thermal impedance ⁽²⁾ | SOP16 | | 87 | $^{\circ}C/W$ |
| | | SSOP16 | | 112 | |
| | | TSSOP16 | | 111 | |
| T_J | Junction temperature ⁽³⁾ | | -40 | 150 | $^{\circ}C$ |
| T_{STG} | Storage temperature | | -65 | +150 | $^{\circ}C$ |

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.

(2) The package thermal impedance is calculated in accordance with JESD-51.

(3) The maximum power dissipation is a function of $T_{J(MAX)}$, $R_{\theta JA}$, and T_A . The maximum allowable power dissipation at any ambient temperature is $P_D = (T_{J(MAX)} - T_A) / R_{\theta JA}$. All numbers apply for packages soldered directly onto a PCB.

8.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

| | | VALUE | UNIT |
|-------------|---|------------|------|
| $V_{(ESD)}$ | Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001 ⁽¹⁾ | ± 6000 | V |
| | Charged-device model (CDM) | ± 1000 | V |
| | Machine model (MM) | ± 400 | V |

(1) JEDEC document JEP155 states that 500 V HBM allows safe manufacturing with a standard ESD control process.



ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

8.3 Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted).

| SYMBOL | PARAMETER | MIN | MAX | UNIT |
|----------|-----------------------|-----|-----|-------------|
| V_{CC} | Supply Voltage | 1.8 | 5.5 | V |
| T_A | Operating temperature | -40 | 125 | $^{\circ}C$ |

8.4 ELECTRICAL CHARACTERISTICS

$V_{CC} = +1.8V$ to $+5.5V$, $FULL = -40^{\circ}C$ to $125^{\circ}C$, Typical values are at $T_A = +25^{\circ}C$. (unless otherwise noted)

| PARAMETER | SYMBOL | CONDITIONS | V_{CC} | T_A | MIN ⁽¹⁾ | TYP ⁽²⁾ | MAX ⁽¹⁾ | UNIT |
|--------------------------------------|-----------------------|--|--------------|-------|--------------------|--------------------|--------------------|----------|
| DC CHARACTERISTICS | | | | | | | | |
| On-Resistance | R_{ON} | $I_A = 13mA$ | 5V | +25°C | | 8 | 11 | Ω |
| | | | | FULL | | | 14 | |
| On-Resistance Match Between Channels | $\Delta R_{ON}^{(3)}$ | $I_A = 13mA$ | 5V | +25°C | | 0.05 | 0.2 | Ω |
| | | | | FULL | | | 0.25 | Ω |
| On-Resistance Flatness | $R_{FLAT(ON)}^{(4)}$ | | 5V | +25°C | | 4.5 | 6 | Ω |
| | | | | FULL | | | 8 | Ω |
| High-level control input Voltage | V_{IH} | | 1.8V | FULL | 1.1 | | | V |
| | | | 2.5V to 5.5V | FULL | 2 | | | |
| Low-level control input Voltage | V_{IL} | | 1.8V | FULL | | | 0.4 | V |
| | | | 2.5V to 5.5V | FULL | | | 0.5 | |
| Input High Current | I_{IH} | $V_{IN} \text{ and } V_{OE} = V_{CC}$ | 5.5V | +25°C | | | ± 1 | μA |
| | | | | FULL | | | ± 2 | |
| Input Low Current | I_{IL} | $V_{IN} \text{ and } V_{OE} = 0V$ | 5.5V | +25°C | | | ± 1 | μA |
| | | | | FULL | | | ± 2 | |
| Analog Output Leakage Current | I_O | $V_{B1} \text{ or } V_{B2} = 3.3V/0.3V$ $V_A = 0.3V/3.3V$ | 5.5V | +25°C | | | ± 1 | μA |
| | | | | FULL | | | ± 2 | |
| Clamp Diode Voltage | V_{IK} | $I_I = -18mA$ | 5.5V | +25°C | | -0.9 | | V |
| DYNAMIC CHARACTERISTICS | | | | | | | | |
| Turn-On Time | t_{ON} | $R_L = 75\Omega, C_L = 20pF$, Test Circuit 1 | 5.5V | +25°C | | 13 | 20 | ns |
| | | | | FULL | | | 23 | |
| | | | 3.3V | +25°C | | 19 | 29 | ns |
| | | | | FULL | | | 32 | |
| Turn-Off Time | t_{OFF} | $R_L = 75\Omega, C_L = 20pF$, Test Circuit 1 | 5.5V | +25°C | | 30 | 55 | ns |
| | | | | FULL | | | 60 | |
| | | | 3.3V | +25°C | | 40 | 60 | ns |
| | | | | FULL | | | 68 | |
| -3dB Bandwidth | BW | $R_L = 150\Omega$, Test Circuit 3 | 5.5V | +25°C | | 220 | | MHz |
| Channel-to-Channel Crosstalk | X_{TALK} | $R_{IN} = 10\Omega, R_L = 150\Omega$, $f = 10MHz$, Test Circuit 4 | 5.5V | +25°C | | -60 | | dB |
| Off Isolation | O_{IRR} | $R_L = 150\Omega, f = 10MHz$, Test Circuit 5 | 5.5V | +25°C | | -52 | | dB |
| Input/Enable Capacitance | C_{IN} | $f = 1MHz$, Test Circuit 5 | 5.5V | +25°C | | 5 | | pF |
| Switch OFF Capacitance | C_{OFF} | $f = 1MHz$, Test Circuit 5 | 5.5V | +25°C | | 9 | | pF |
| Switch ON Capacitance | C_{ON} | $f = 1MHz$, Test Circuit 5 | 5.5V | +25°C | | 18 | | pF |
| Differential Gain | D_G | $R_L = 150\Omega, f = 3.58MHz$, Test Circuit 2 | 5.5V | +25°C | | 0.5 | | % |
| Differential Phase | D_P | $R_L = 150\Omega, f = 3.58MHz$, Test Circuit 2 | 5.5V | +25°C | | 0.05 | | ° |

| POWER REQUIREMENTS | | | | | | | |
|--|------------------|---|------|-------|-----|-----|-----|
| Power Supply Range | V _{CC} | | | FULL | 1.8 | | 5.5 |
| Power Supply Current | I _{CC} | V _{IN} and V _{OE} = 5V/0V | 5.5V | +25°C | | 0.1 | 1 |
| | | | | FULL | | | 2 |
| Supply Current per Input @ TTL HIGH | ΔI _{CC} | V _{IN} or V _{OE} = 3.4V | 5.5V | +25°C | | 100 | |
| | | | | FULL | | 200 | μA |

(1) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.

(2) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.

(3) This parameter is ensured by design and/or characterization and is not tested in production.

(4) Flatness is defined as the difference between the maximum and minimum values of ON-state resistance over the specified range of conditions.

9 TYPICAL CHARACTERISTICS

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.

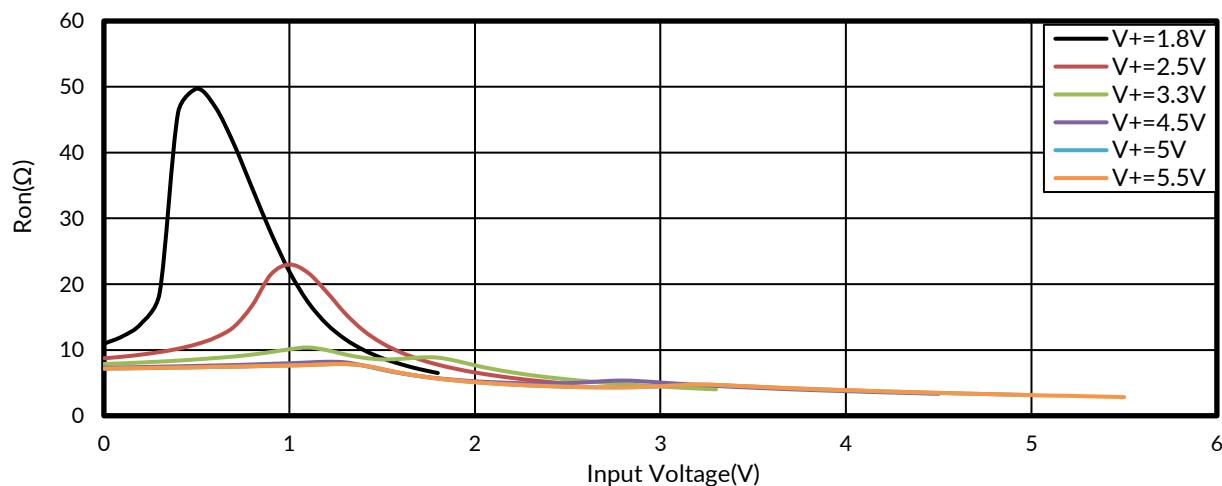
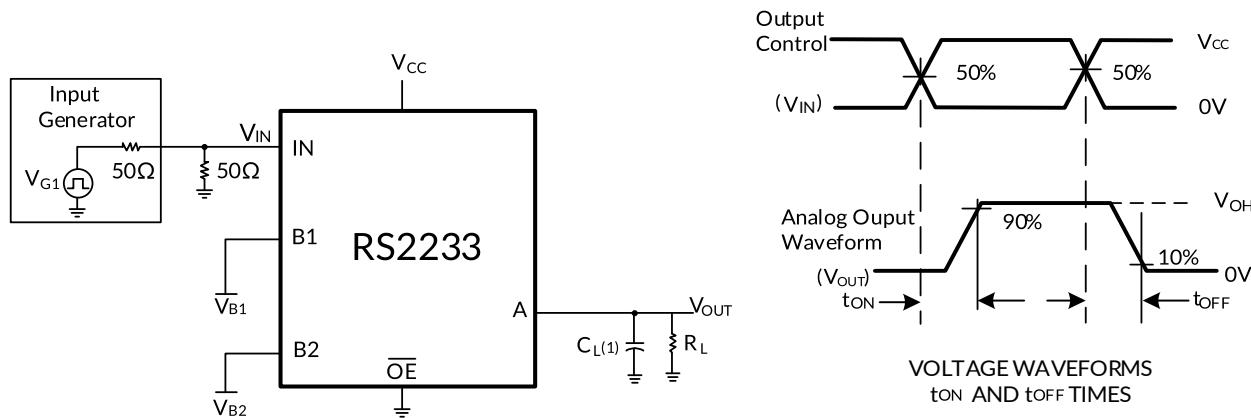


Figure 1. Typical Ron as a Function of Input Voltage

10 PARAMETER MEASUREMENT INFORMATION



| Test | V _{CC} | R _L | C _L | V _{B1} | V _{B2} |
|------------------|-----------------|----------------|----------------|-----------------|-----------------|
| t _{ON} | 5V±0.5V | 75Ω | 20pF | GND | 3V |
| | 5V±0.5V | 75Ω | 20pF | 3V | GND |
| t _{OFF} | 5V±0.5V | 75Ω | 20pF | GND | 3V |
| | 5V±0.5V | 75Ω | 20pF | 3V | GND |

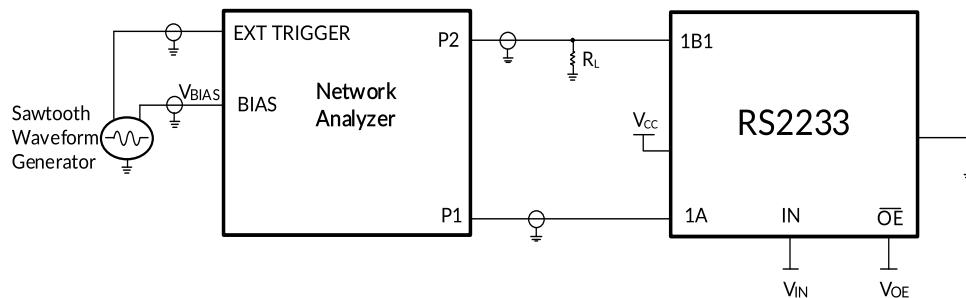
NOTES:

1. CL includes probe and jig capacitance.

2. All input pulses are supplied by generators having the following characteristics: PRR≤10MHz, Z_o=50Ω, t_r≤2.5ns, t_f≤2.5ns.

3. The outputs are measured one at a time, with one transition per measurement.

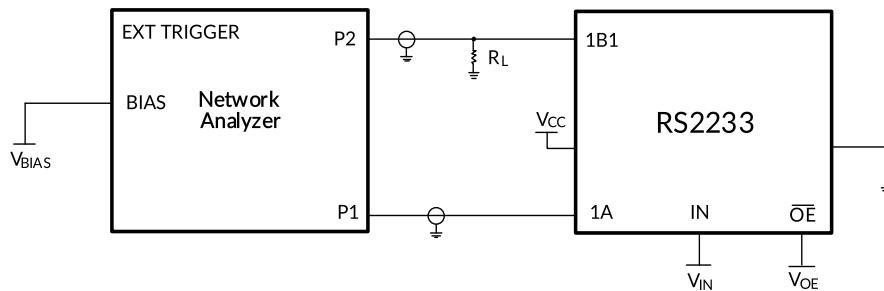
Test Circuit 1. Test Circuit for Voltage Waveform and Switch Time



NOTES: Differential gain and phase are measured at the output of the ON channel. For example, when V_{IN} = 0, V_{OE} = 0, and 1A is the input, the output is measured at 1B1.

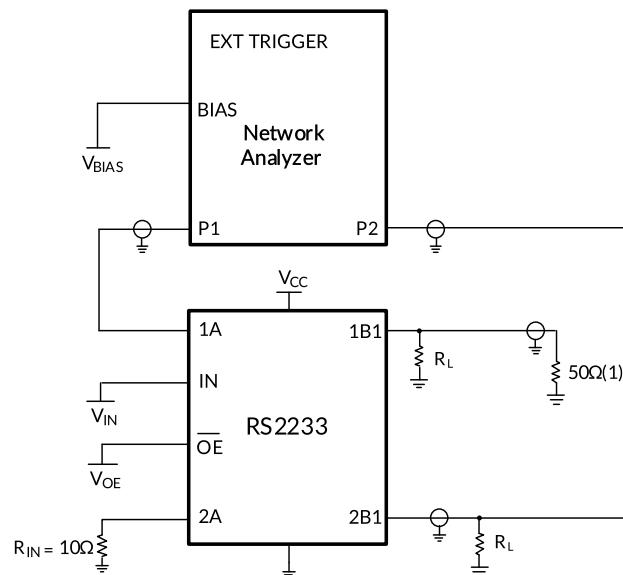
Test Circuit 2. Test Circuit for Differential Gain/Phase Measurement

PARAMETER MEASUREMENT INFORMATION



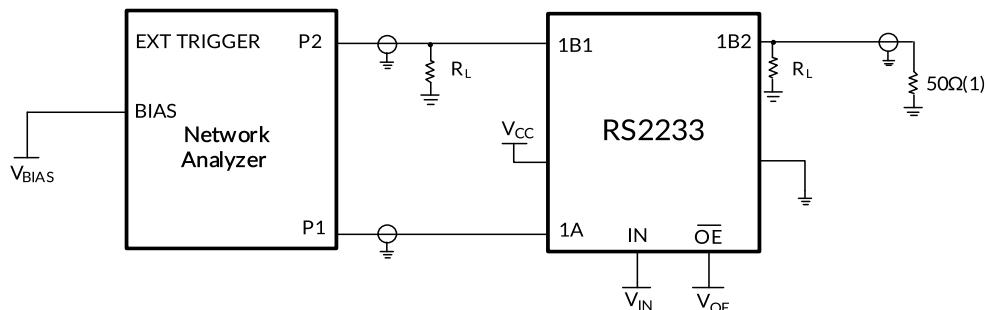
NOTES: Frequency response is measured at the output of the ON channel. For example, when $V_{IN} = 0$, $V_{EN} = 0$, and 1A is the input, the output is measured at 1B1. All unused analog I/O ports are left open.

Test Circuit 3. Test Circuit for Frequency Response (BW)



NOTE: 1. A 50Ω termination resistor is needed for the network analyzer.

Test Circuit 4. Test Circuit for Crosstalk (XTALK)

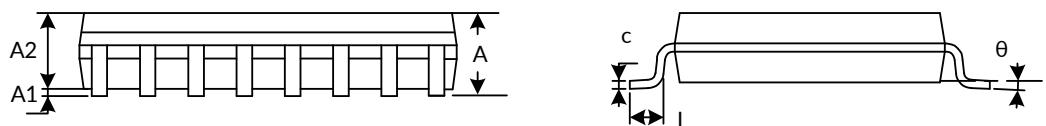
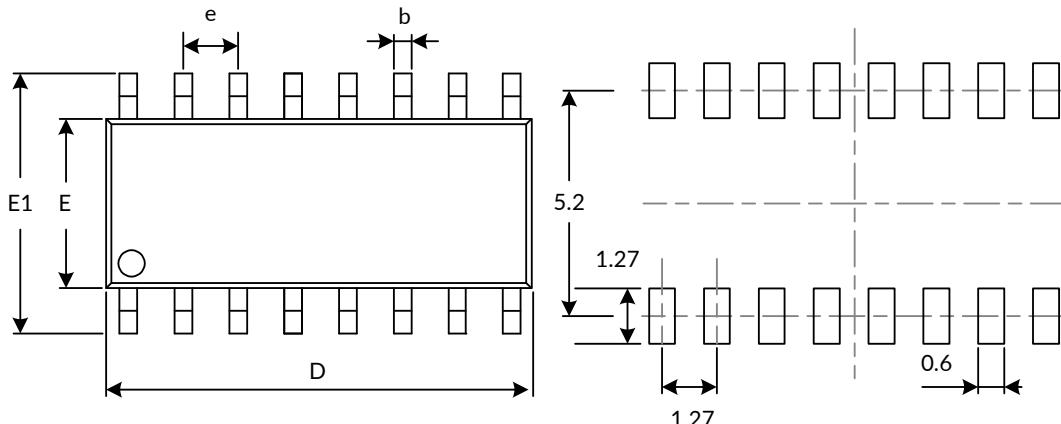


NOTE: 1. A 50Ω termination resistor is needed for the network analyzer.

Test Circuit 5. Test Circuit for Off Isolation (OIRR)

11 PACKAGE OUTLINE DIMENSIONS

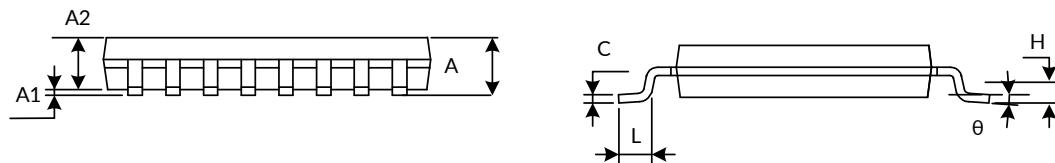
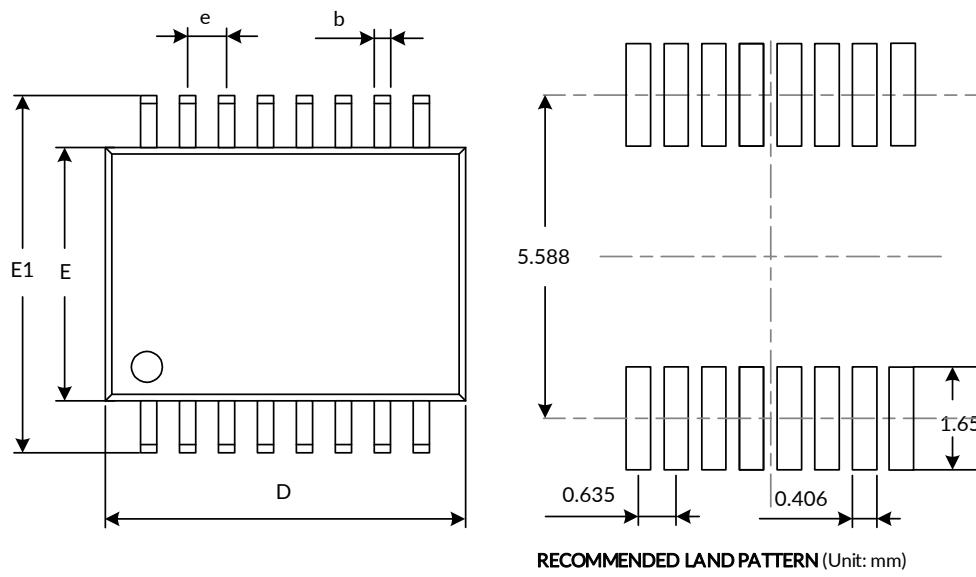
SOP16⁽³⁾



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|------------------|---------------------------|--------|---------------------------|-------|
| | Min | Max | Min | Max |
| A ⁽¹⁾ | 1.350 | 1.750 | 0.053 | 0.069 |
| A1 | 0.100 | 0.250 | 0.004 | 0.010 |
| A2 | 1.350 | 1.550 | 0.053 | 0.061 |
| b | 0.330 | 0.510 | 0.013 | 0.020 |
| c | 0.170 | 0.250 | 0.006 | 0.010 |
| D ⁽¹⁾ | 9.800 | 10.200 | 0.386 | 0.402 |
| E ⁽¹⁾ | 3.800 | 4.000 | 0.150 | 0.157 |
| E1 | 5.800 | 6.200 | 0.228 | 0.244 |
| e | 1.270(BSC) ⁽²⁾ | | 0.050(BSC) ⁽²⁾ | |
| L | 0.400 | 1.270 | 0.016 | 0.050 |
| θ | 0° | 8° | 0° | 8° |

NOTE:

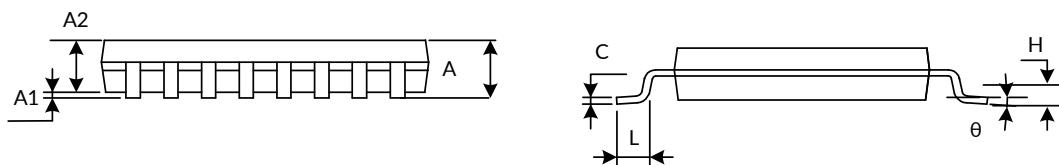
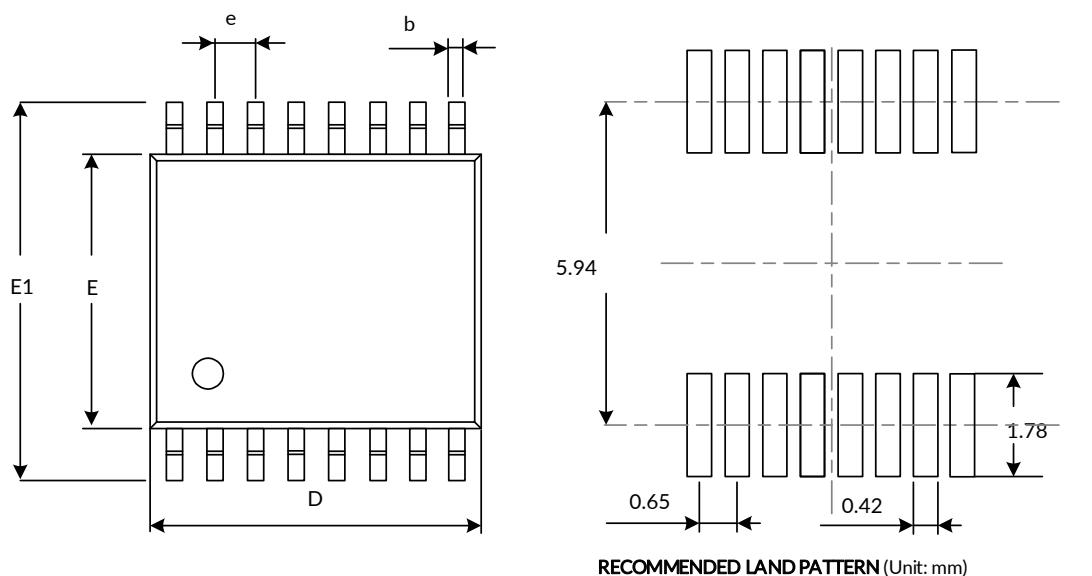
1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.

SSOP16⁽³⁾


| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|------------------|----------------------------------|------------|-----------------------------|------------|
| | Min | Max | Min | Max |
| A ⁽¹⁾ | 1.350 | 1.750 | 0.053 | 0.069 |
| A1 | 0.100 | 0.250 | 0.004 | 0.010 |
| A2 | 1.350 | 1.550 | 0.053 | 0.061 |
| b | 0.200 | 0.300 | 0.008 | 0.012 |
| c | 0.170 | 0.250 | 0.007 | 0.010 |
| D ⁽¹⁾ | 4.700 | 5.100 | 0.185 | 0.200 |
| E ⁽¹⁾ | 3.800 | 4.000 | 0.150 | 0.157 |
| E1 | 5.800 | 6.200 | 0.228 | 0.244 |
| e | 0.635(BSC) ⁽²⁾ | | 0.025(BSC) ⁽²⁾ | |
| L | 0.400 | 1.270 | 0.016 | 0.050 |
| θ | 0° | 8° | 0° | 8° |

NOTE:

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.

TSSOP16⁽³⁾


| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|------------------|---------------------------|-------|---------------------------|-------|
| | Min | Max | Min | Max |
| A ⁽¹⁾ | | 1.200 | | 0.047 |
| A1 | 0.050 | 0.150 | 0.002 | 0.006 |
| A2 | 0.800 | 1.050 | 0.031 | 0.041 |
| b | 0.190 | 0.300 | 0.007 | 0.012 |
| c | 0.090 | 0.200 | 0.004 | 0.008 |
| D ⁽¹⁾ | 4.860 | 5.100 | 0.191 | 0.201 |
| E ⁽¹⁾ | 4.300 | 4.500 | 0.169 | 0.177 |
| E1 | 6.200 | 6.600 | 0.244 | 0.260 |
| e | 0.650(BSC) ⁽²⁾ | | 0.026(BSC) ⁽²⁾ | |
| L | 0.500 | 0.700 | 0.02 | 0.028 |
| H | 0.25TYP | | 0.01TYP | |
| θ | 1° | 7° | 1° | 7° |

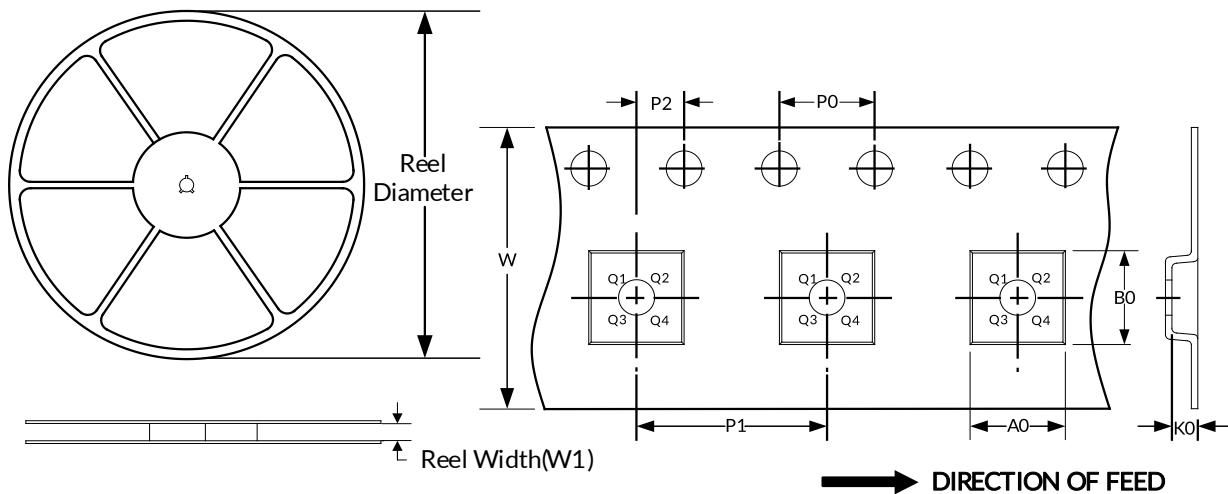
NOTE:

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.

12 TAPE AND REEL INFORMATION

REEL DIMENSIONS

TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

| Package Type | Reel Diameter | Reel Width(mm) | A0 (mm) | B0 (mm) | K0 (mm) | P0 (mm) | P1 (mm) | P2 (mm) | W (mm) | Pin1 Quadrant |
|--------------|---------------|----------------|---------|---------|---------|---------|---------|---------|--------|---------------|
| TSSOP16 | 13" | 12.4 | 6.90 | 5.60 | 1.20 | 4.0 | 8.0 | 2.0 | 12.0 | Q1 |
| SSOP16 | 13" | 12.4 | 8.30 | 6.70 | 2.10 | 4.0 | 8.0 | 2.0 | 12.0 | Q1 |
| SOP16 | 13" | 16.4 | 6.50 | 10.30 | 2.10 | 4.0 | 8.0 | 2.0 | 16.0 | Q1 |

NOTE:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.

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