

Low-Noise, Very Low Drift, Precision Voltage Reference

1 FEATURES

- **Low Temperature Drift: 20 ppm/°C(Max)**
- **High Accuracy: 0.1% Maximum**
- **Low Noise: 35μV_{PP}/V**
- **Low I_Q: 150μA (Typical)**
- **Operating Temperature Range: -40°C to +125°C**
- **High Output Current: ±10 mA**
- **Micro SIZE PACKAGES: SOP8、SOT23**

2 APPLICATIONS

- **Precision Data Acquisition Systems**
- **Semiconductor Test Equipment**
- **Medical Instrumentation**
- **Industrial Process Controls**
- **Pressure and Temperature Transmitters**
- **Lab and Field Instrumentation**

3 DESCRIPTIONS

The RS3112 is a family of low-noise, low-drift, very high precision voltage references. These references are capable of both sinking and sourcing current, and have excellent line and load regulation.

Excellent temperature drift (20ppm/°C) and high accuracy (0.1%) are achieved using proprietary design techniques with 150µA(typical) quiescent current. These features, combined with low noise, make the RS3112 family ideal for use in high-precision data acquisition systems.

The RS3112 is available in Green SOP8 and SOT23 packages. It operates over an ambient temperature range of -40°C to +125°C.

Device Information⁽¹⁾

Device Information		
Part Number	Package	Body Size(NOM)
RS3112	SOP8	4.90mm x 3.90mm
	SOT23	1.30mm×2.90mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

4 TYPICAL APPLICATION

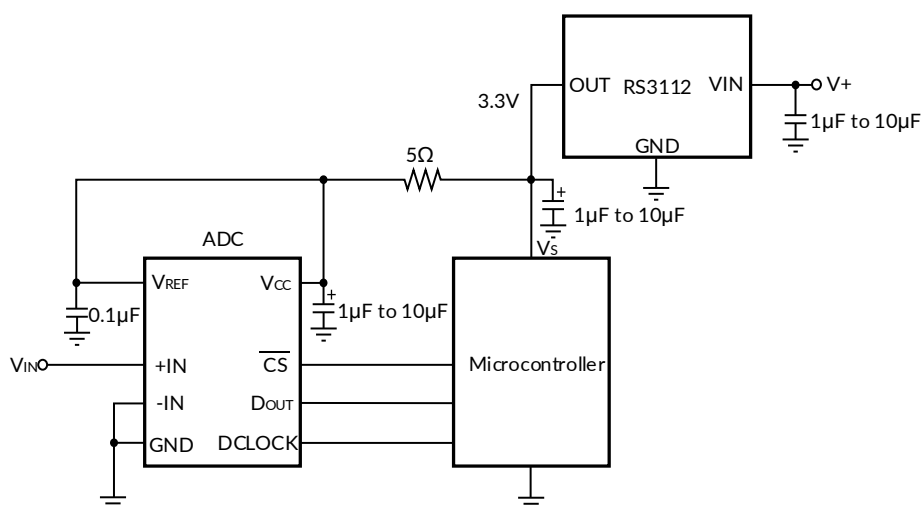


Table of Contents

1 FEATURES	1
2 APPLICATIONS	1
3 DESCRIPTIONS	1
4 TYPICAL APPLICATION	1
5 REVISION HISTORY	3
6 PACKAGE/ORDERING INFORMATION ⁽¹⁾	4
7 PIN CONFIGURATION AND FUNCTIONS	5
8 SPECIFICATIONS	6
8.1 Absolute Maximum Ratings	6
8.2 ESD Ratings	6
8.3 Recommended Operating Conditions	6
8.4 Electrical Characteristics	7
8.5 Typical Characteristics	8
9 DETAILED DESCRIPTION	11
9.1 Overview	11
9.2 Functional Block Diagram	11
10 FEATURE DESCRIPTION	12
10.1 Temperature Drift	12
10.2 Thermal Hysteresis	12
10.3 Noise Performance	12
10.4 Long-Term Stability	12
11 DEVICE FUNCTIONAL MODES	13
11.1 Basic Connections	13
11.2 Supply Voltage	13
11.3 Negative Reference Voltage	13
12 LAYOUT	14
12.1 Layout Guidelines	14
12.2 Layout Example	14
12.3 Power Dissipation	14
13 PACKAGE OUTLINE DIMENSIONS	15
14 TAPE AND REEL INFORMATION	17

5 REVISION HISTORY

Note: Page numbers for previous revisions may different from page numbers in the current version.

VERSION	Change Date	Change Item
A.0	2021/11/18	Preview version completed
A.1	2022/02/22	1.Add Functional Block Diagram 2.Add Detailed Description 3.Add SOT23 package
A.2	2023/01/04	1.Update FEATURES and DESCRIPTIONS on Page 1 in RevA.1 2.Update ELECTRICAL CHARACTERISTICS on Page 6 in RevA.1
A.3	2023/03/17	Delete 1.8V and 4.5V Output Voltage
A.4	2023/08/24	Update ELECTRICAL CHARACTERISTICS
A.4.1	2024/02/23	Modify packaging naming
A.5	2024/04/16	1. Add 1.8V and 4.5V Output Voltage 2. Add MSL on Page 4 in RevA.4.1
A.6	2025/12/02	1. Update dV_{OUT}/dT parameter values in Electrical Characteristics 2. Add figures of Temperature Drift in Typical Characteristics

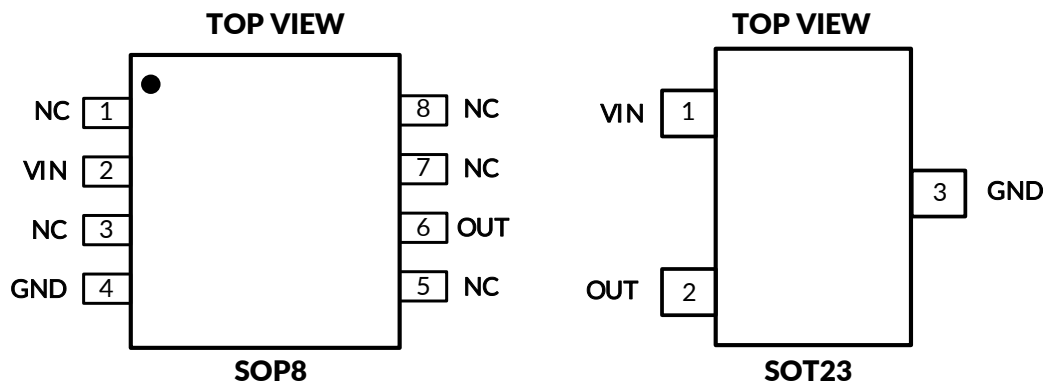
6 PACKAGE/ORDERING INFORMATION ⁽¹⁾

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING ^(2/3)	MSL ⁽⁴⁾	PACKAGE OPTION
RS3112	RS3112-1.25XK	-40°C ~+125°C	SOP8	RS3112A	MSL1	Tape and Reel, 4000
	RS3112-1.8XK	-40°C ~+125°C	SOP8	RS3112B	MSL1	Tape and Reel, 4000
	RS3112-2.048XK	-40°C ~+125°C	SOP8	RS3112C	MSL1	Tape and Reel, 4000
	RS3112-2.5XK	-40°C ~+125°C	SOP8	RS3112D	MSL1	Tape and Reel, 4000
	RS3112-3.0XK	-40°C ~+125°C	SOP8	RS3112E	MSL1	Tape and Reel, 4000
	RS3112-3.3XK	-40°C ~+125°C	SOP8	RS3112F	MSL1	Tape and Reel, 4000
	RS3112-4.096XK	-40°C ~+125°C	SOP8	RS3112G	MSL1	Tape and Reel, 4000
	RS3112-4.5XK	-40°C ~+125°C	SOP8	RS3112H	MSL1	Tape and Reel, 4000
	RS3112-1.25XSF3	-40°C ~+125°C	SOT23	3112A	MSL1	Tape and Reel, 3000
	RS3112-1.8XSF3	-40°C ~+125°C	SOT23	3112B	MSL1	Tape and Reel, 3000
	RS3112-2.048XSF3	-40°C ~+125°C	SOT23	3112C	MSL1	Tape and Reel, 3000
	RS3112-2.5XSF3	-40°C ~+125°C	SOT23	3112D	MSL1	Tape and Reel, 3000
	RS3112-3.0XSF3	-40°C ~+125°C	SOT23	3112E	MSL1	Tape and Reel, 3000
	RS3112-3.3XSF3	-40°C ~+125°C	SOT23	3112F	MSL1	Tape and Reel, 3000
	RS3112-4.096XSF3	-40°C ~+125°C	SOT23	3112G	MSL1	Tape and Reel, 3000
	RS3112-4.5XSF3	-40°C ~+125°C	SOT23	3112H	MSL1	Tape and Reel, 3000

NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.
- (3) A, B, C, D, E, F, G, H represents different Reset Thresholds.
- (4) RUNIC classify the MSL level with using the common preconditioning setting in our assembly factory conforming to the JEDEC industrial standard J-STD-20F. Please align with RUNIC if your end application is quite critical to the preconditioning setting or if you have special requirement.

7 PIN CONFIGURATION AND FUNCTIONS



Pin Description

NAME	PIN		I/O ⁽¹⁾	DESCRIPTION
	SOP8	SOT23		
NC ⁽²⁾	1,3,5,7,8	—	—	No internal connection
VIN	2	1	I	Input supply voltage
GND	4	3	—	Ground
OUT	6	2	O	Reference output voltage

(1) I = Input, O = Output.

(2) There is no internal connection. Typically, GND is the recommended connection to a heat spreading plane.

8 SPECIFICATIONS

8.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted) ⁽¹⁾⁽²⁾

SYMBOL		MIN	MAX	UNIT
V _{IN}	Supply voltage, V+ to V-	-0.2	6.0	V
	Output short circuit	-30	30	mA
θ _{JA}	Package thermal impedance ⁽³⁾	SOT23	292.9	°C/W
		SOP8	110.88	
T _A	Operating temperature	-40	+125	°C
T _J	Junction temperature ⁽⁴⁾	-40	150	
T _{stg}	Storage temperature	-65	150	

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) All voltages are with respect to the GND pin.

(3) The package thermal impedance is calculated in accordance with JESD-51.

(4) The maximum power dissipation is a function of T_{J(MAX)}, R_{θJA}, and T_A. The maximum allowable power dissipation at any ambient temperature is P_D = (T_{J(MAX)} - T_A) / R_{θJA}. All numbers apply for packages soldered directly onto a PCB.

8.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

		VALUE	UNIT
V _(ESD) Electrostatic discharge	Human-Body Model (HBM), per ANSI/ESDA/JEDEC JS-001 ⁽¹⁾	±4000	V
	Charged-Device Model (CDM), per ANSI/ESDA/JEDEC JS-002 ⁽²⁾	±1500	V
	Machine Model (MM)	±400	V

(1) JEDEC document JEP155 states that 500 V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250 V CDM allows safe manufacturing with a standard ESD control process.



ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

8.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

SYMBOL	PARAMETER	MIN	MAX	UNIT
V _{IN}	Input voltage	V _{OUT} +0.3 ⁽¹⁾	5.5	V
I _{Load}	Load current	-10	10	mA

(1) Except for the RS3112-1.25/2.048, where V_{IN}(min) = 2.45 V.

8.4 Electrical Characteristics

At $T_A = 25^\circ\text{C}$, $I_{\text{OUT}} = 0\text{ mA}$, and $V_{\text{IN}} = 5\text{ V}$ (unless otherwise noted)

PARAMETER			TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V _{OUT}		RS3112-1.25V		1.25		V
			RS3112-1.8V		1.8		
			RS3112-2.048V		2.048		
			RS3112-2.5V		2.5		
			RS3112-3.0V		3.0		
			RS3112-3.3V		3.3		
			RS3112-4.096V		4.096		
			RS3112-4.5V		4.5		
Initial Accuracy			All voltage options ⁽¹⁾	-0.1		0.1	%
Output Voltage Noise			f = 0.1Hz to 10Hz		35		μV _{PP} /V
Output Voltage Temperature Drift ⁽²⁾	dV _{OUT} /dT		T _A = 0°C to 70°C ⁽⁴⁾		2.6	9	ppm/°C
			T _A = -40°C to 105°C ⁽⁴⁾		2.9	8	
			T _A = -40°C to 125°C		4	20	
Long-Term Stability			0 to 1000 hours		100		ppm
Line Regulation			V _{IN} = (V _{OUT} + 0.3) to 5.5 V (V _{OUT} < 4V)		25	70	ppm/V
			V _{IN} = (V _{OUT} + 0.3) to 5.5 V (V _{OUT} < 4V) T _A = -40°C to +125°C			150	
			V _{IN} = (V _{OUT} + 0.3) to 5.5 V (V _{OUT} > 4V)		50	200	
			V _{IN} = (V _{OUT} + 0.3) to 5.5 V (V _{OUT} > 4V) T _A = -40°C to +125°C			260	
Load Regulation	dV _{OUT} /dI _{LOAD}		-10 mA < I _{LOAD} < 10 mA, V _{IN} = V _{OUT} + 0.3 V ⁽³⁾		1.5	10	ppm/mA
			-10 mA < I _{LOAD} < 10 mA, V _{IN} = V _{OUT} + 0.3 V T _A = -40°C to 125°C ⁽³⁾			15	
Thermal Hysteresis	dT		First Cycle		100		ppm
Short-Circuit Current	I _{SC}	Sourcing			26		mA
		Sinking			27		
Turn on Settling Time			To 0.1% with C _L = 1μF		200		μs
Capacitive Load				1		50	μF
Voltage	V _{IN}		I _{LOAD} = 0, T _A = -40°C to +125°C.	V _{OUT} + 0.3 ⁽¹⁾		5.5	V
Quiescent Current	I _Q		I _{LOAD} = 0, T _A = 25°C		150	180	μA
			I _{LOAD} = 0, T _A = -40°C to +125°C			220	

(1) Minimum supply voltage for the RS3112 is 2.45 V.

(2) Box Method used to determine temperature drift.

(3) Typical value of load regulation reflects measurements using force and sense contacts;

(4) The parameter is labeled based on laboratory test data from 210 samples.

8.5 Typical Characteristics

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.

At $T_A = 25^\circ\text{C}$, $V_{IN} = 5\text{V}$ power supply, and RS3112 is used for typical characteristic measurements, unless otherwise noted.

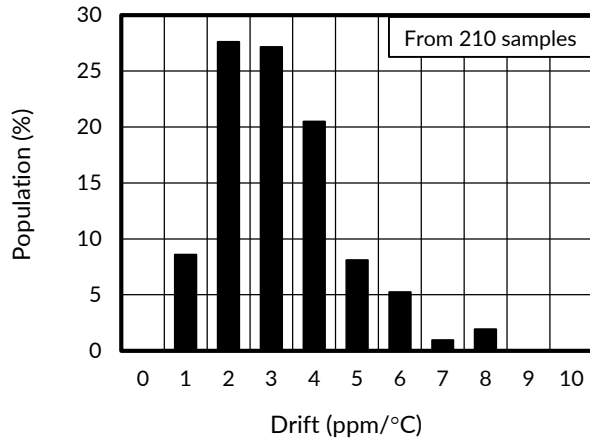


Figure 1. Temperature Drift (0°C to 70°C)

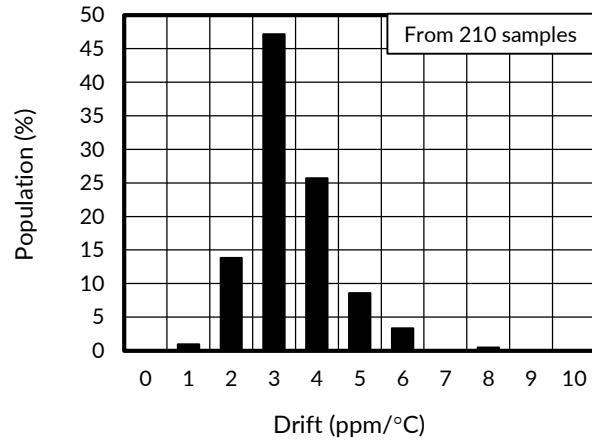


Figure 2. Temperature Drift (-40°C to 105°C)

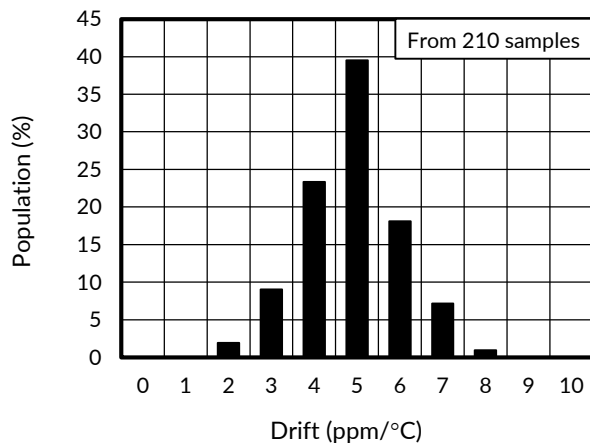


Figure 3. Temperature Drift (-40°C to 125°C)

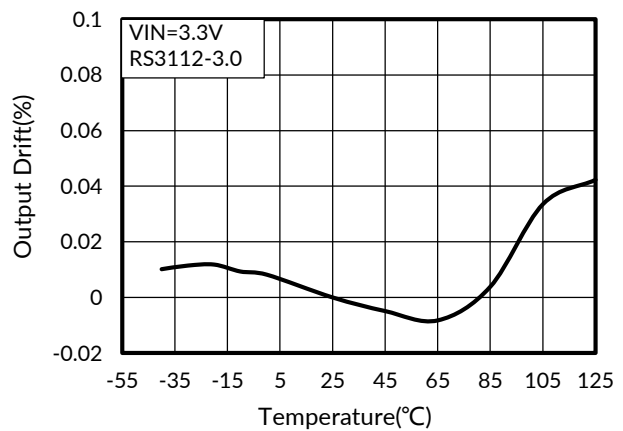


Figure 4. Output Voltage vs Temperature

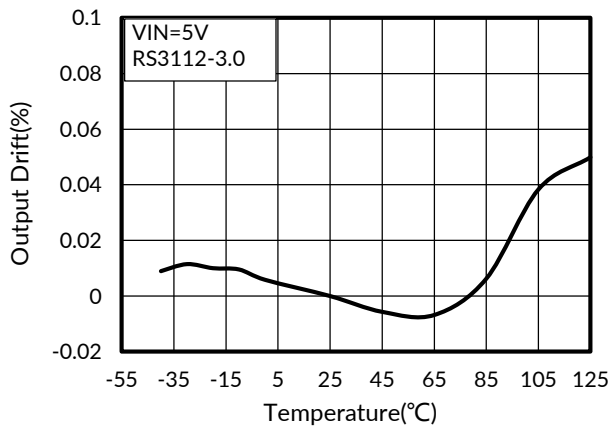


Figure 5. Output Voltage vs Temperature

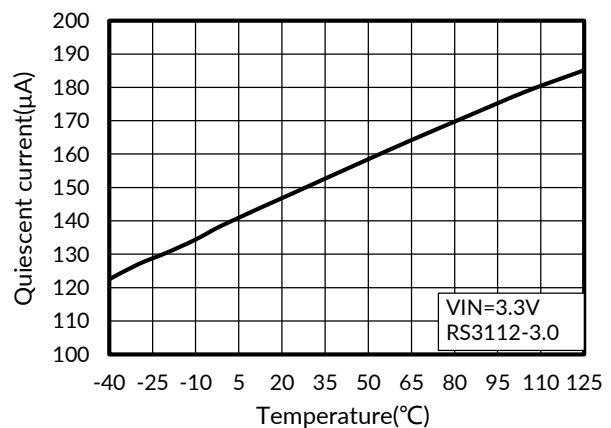


Figure 6. Quiescent current vs Temperature

Typical Characteristics

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.

At $T_A = 25^\circ\text{C}$, $V_{IN} = 5\text{V}$ power supply, and RS3112 is used for typical characteristic measurements, unless otherwise noted.

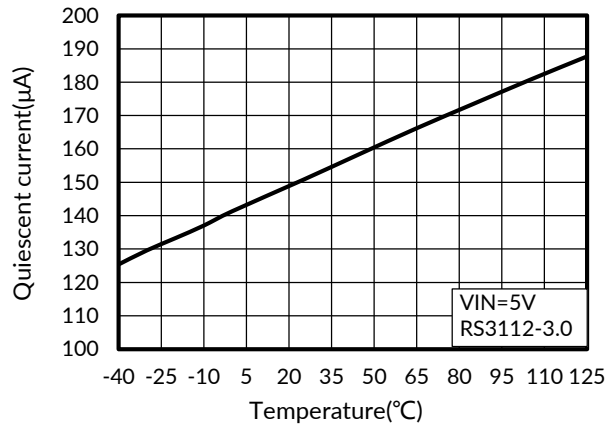


Figure 7. Quiescent current vs Temperature

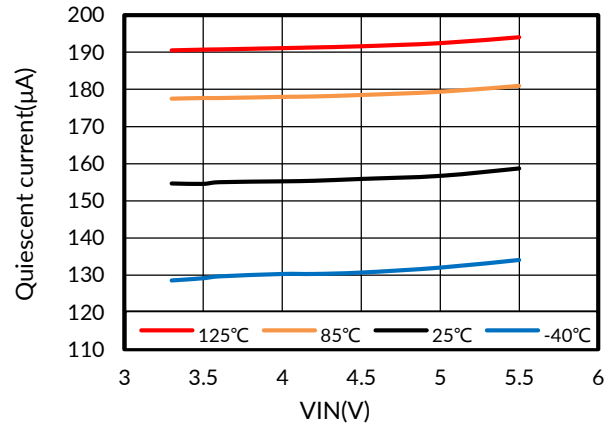


Figure 8. Quiescent current vs Supply Voltage

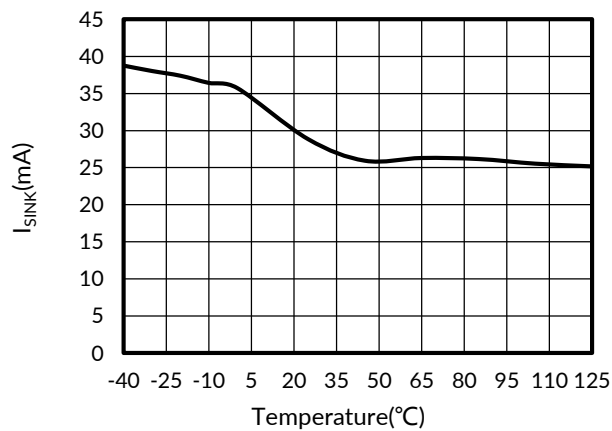


Figure 9. Sink current vs Temperature

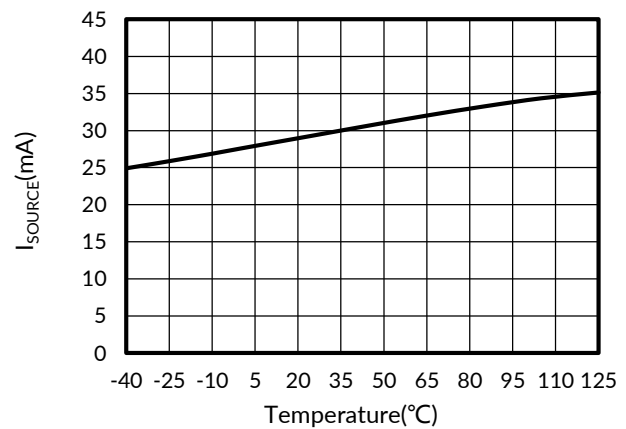


Figure 10. Source current vs Temperature

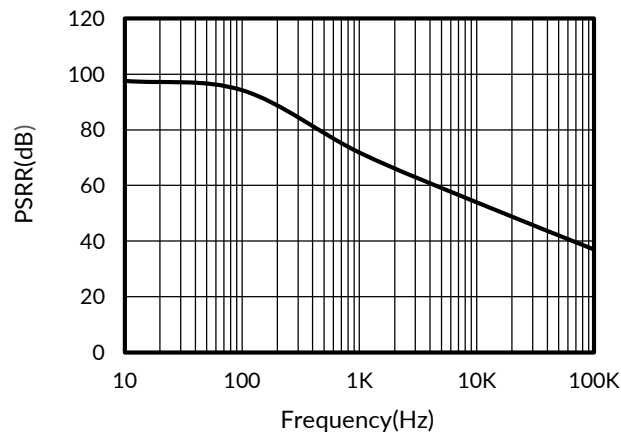


Figure 11. PSRR vs Frequency

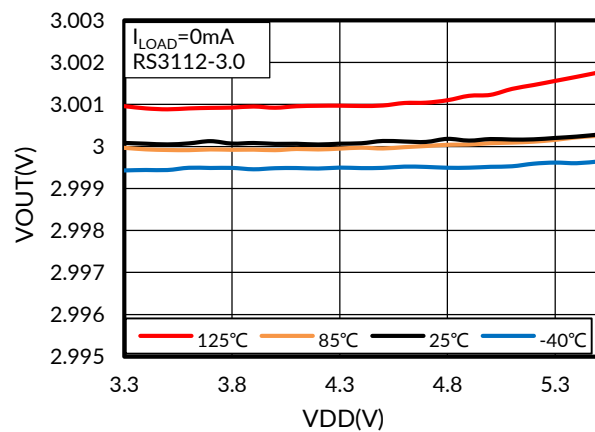


Figure 12. Output Voltage vs Supply Voltage

Typical Characteristics

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.

At $T_A = 25^\circ\text{C}$, $V_{IN} = 5\text{V}$ power supply, and RS3112 is used for typical characteristic measurements, unless otherwise noted.

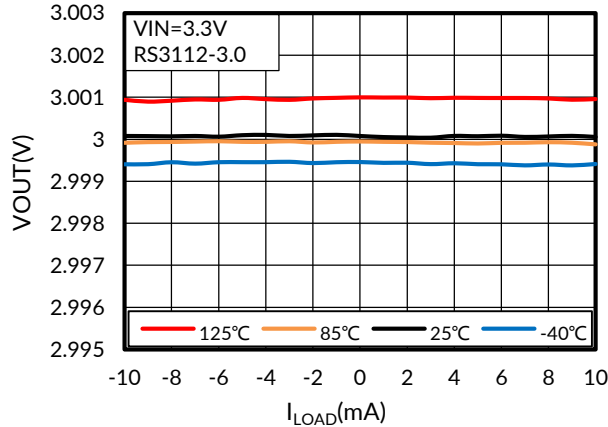


Figure 13. Output Voltage vs Output Current

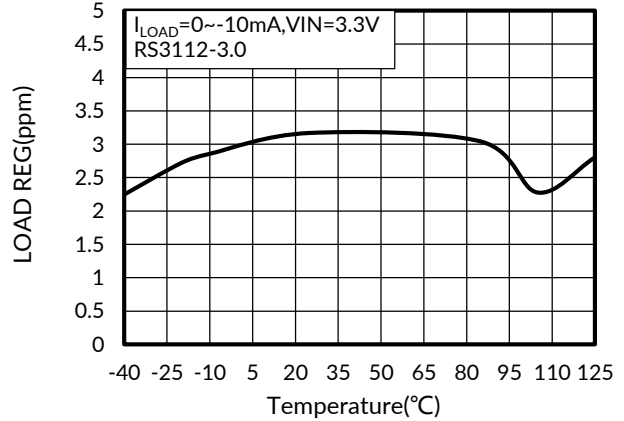


Figure 14. Load Regulation vs Temperature

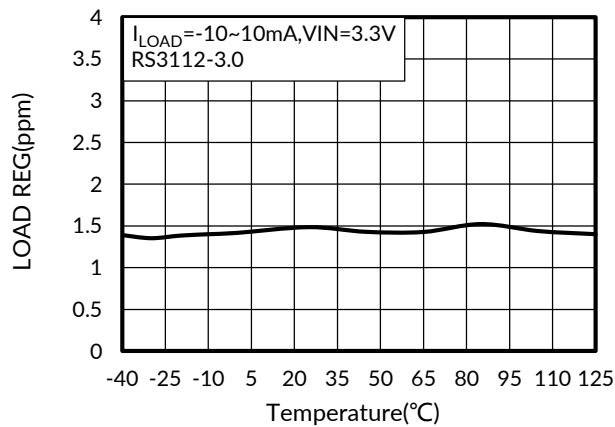


Figure 15. Load Regulation vs Temperature

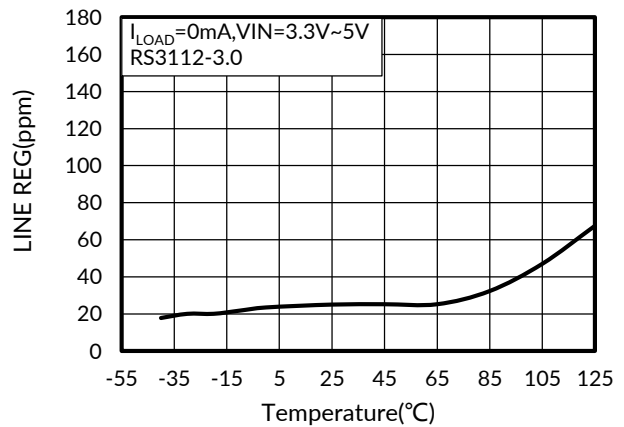


Figure 16. LINE Regulation vs Temperature

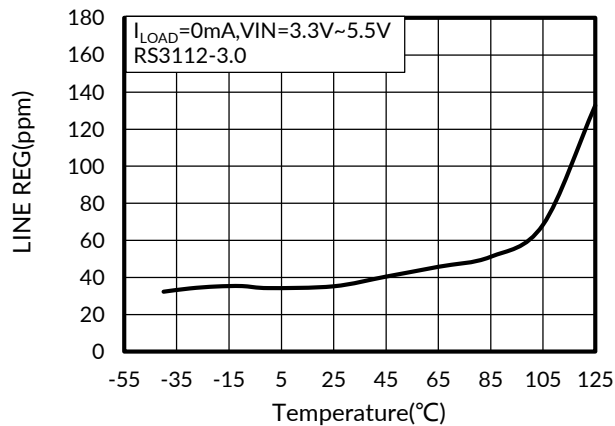


Figure 17. LINE Regulation vs Temperature

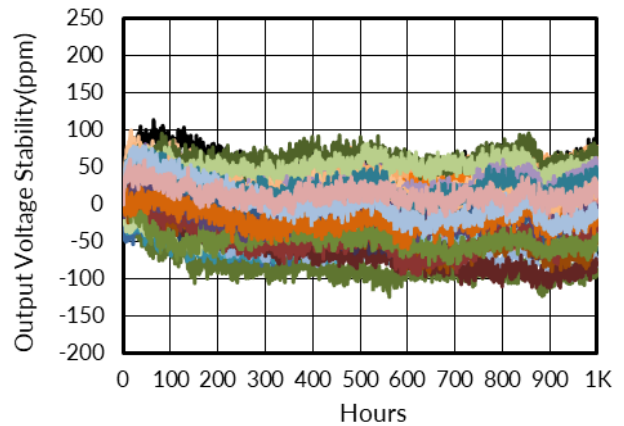


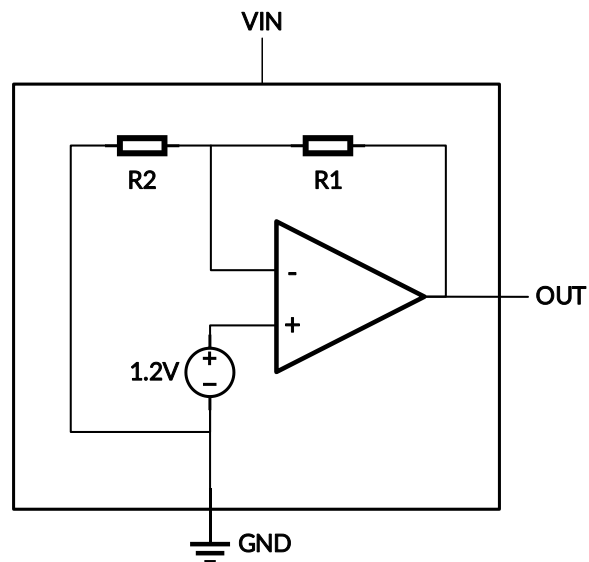
Figure 18. Long-Term Stability (0-1000 Hours)

9 DETAILED DESCRIPTION

9.1 Overview

The RS3112 is family of precision bandgap voltage references that are specifically designed for excellent initial voltage accuracy and drift. See the Functional Block Diagram for a simplified block diagram of the RS3112.

9.2 Functional Block Diagram



10 FEATURE DESCRIPTION

10.1 Temperature Drift

The RS3112 is designed for minimal drift error, which is defined as the change in output voltage over temperature. The drift is calculated using the box method, as described in Equation 1.

$$\text{Drift} = \left(\frac{V_{\text{OUTMAX}} - V_{\text{OUTMIN}}}{V_{\text{OUT}} \times \text{Temp Range}} \right) \times 10^6 (\text{ppm}) \quad (1)$$

The RS3112 features a maximum drift coefficient of 20 ppm/°C.

10.2 Thermal Hysteresis

Thermal hysteresis for the RS3112 is defined as the change in output voltage after operating the device at 25°C, cycling the device through the specified temperature range, and returning to 25°C. Thermal hysteresis can be expressed as Equation 2:

$$V_{\text{HYST}} = \left(\frac{|V_{\text{PRE}} - V_{\text{POST}}|}{V_{\text{NOM}}} \right) \bullet 10^6 (\text{ppm})$$

where

- V_{HYST} = thermal hysteresis (in units of ppm).
- V_{NOM} = the specified output voltage.
- V_{PRE} = output voltage measured at 25°C pretemperature cycling.
- V_{POST} = output voltage measured after the device has been cycled from 25°C through the specified temperature range of -40°C to 125°C and returned to 25°C.

(2)

10.3 Noise Performance

Typical 0.1Hz to 10Hz voltage noise for each member of the RS3112 family is specified in the Electrical Characteristics table. The noise voltage increases with output voltage and operating temperature. Additional filtering can be used to improve output noise levels, although take care to ensure the output impedance does not degrade performance.

10.4 Long-Term Stability

Due to aging and environmental effects, all semiconductor devices experience physical changes of the semiconductor die and the packaging material over time. These changes and the associated package stress on the die cause the output voltage in precision voltage references to deviate over time. The value of such change is specified on the datasheet by a parameter called the Long-term stability (also known as the Long-Term Drift (LTD)). Equation 3 shows how LTD is calculated. Note that the LTD value will be positive if the output voltage drifts higher over time, negative if the voltage drifts lower over time.

$$\text{LTD}(\text{ppm})|_{t=n} = \frac{(V_{\text{OUT}}|_{t=0} - V_{\text{OUT}}|_{t=n})}{V_{\text{OUT}}|_{t=0}} \times 10^6$$

where

- $\text{LTD}(\text{ppm})|_{t=n}$ = Long-term stability (in units of ppm).
- $V_{\text{OUT}}|_{t=0}$ = Output voltage at time = 0 hr.
- $V_{\text{OUT}}|_{t=n}$ = Output voltage at time = n hr.

(3)

11 DEVICE FUNCTIONAL MODES

11.1 Basic Connections

Figure 19 shows the typical connections for the RS3112. RUNIC recommends a supply bypass capacitor ranging from 1 μF to 10 μF . A minimum 1 μF output capacitor (C_L) must be connected from V_{OUT} to GND.

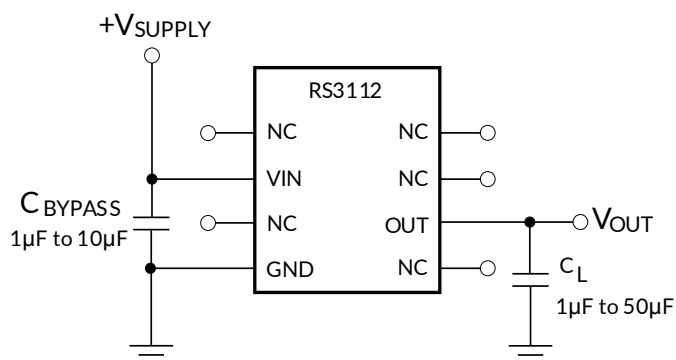


Figure 19. Basic Connections

11.2 Supply Voltage

The RS3112 family of voltage references features extremely low dropout voltage. With the exception of the $V_{OUT} < 2.5\text{V}$, which has a minimum supply requirement of 2.45 V, these references can be operated with a supply of 300 mV more than the output voltage in an unloaded condition.

11.3 Negative Reference Voltage

For applications requiring a negative and positive reference voltage, the RS3112 and RS8651 can be used to provide a dual-supply reference from a 5V supply. Figure 20 shows the RS3112 used to provide a 2.5V supply reference voltage. The low drift performance of the RS3112 complements the low offset voltage and zero drift of the RS8651 to provide an accurate solution for split-supply applications. Take care to match the temperature coefficients of R_1 and R_2 .

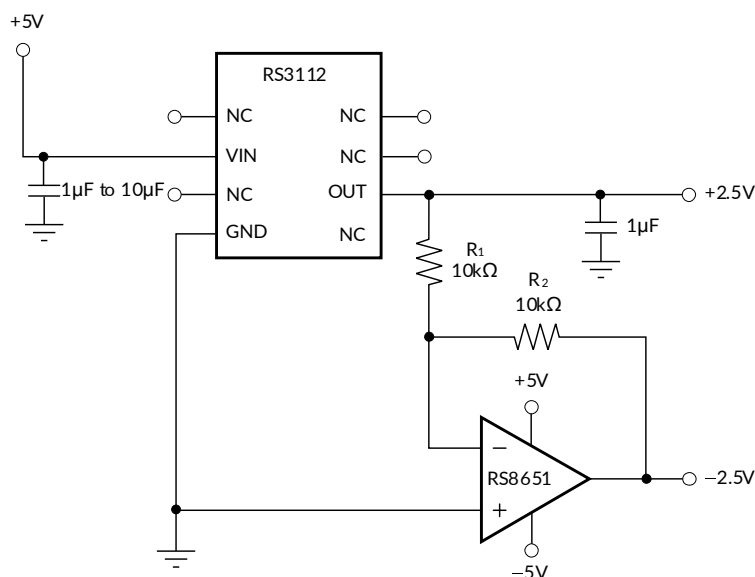


Figure 20. The RS3112 and RS8651 Create Positive and Negative Reference Voltages

12 LAYOUT

12.1 Layout Guidelines

- Place the power-supply bypass capacitor as closely as possible to the supply and ground pins. The recommended value of this bypass capacitor is from 1 μF to 10 μF . If necessary, additional decoupling capacitance can be added to compensate for noisy or high-impedance power supplies.
- Place a 1 μF noise filtering capacitor between the NR pin and ground.
- The output must be decoupled with a 1 μF to 50 μF capacitor. A resistor in series with the output capacitor is optional. For better noise performance, the recommended ESR on the output capacitor is from 1 Ω to 1.5 Ω .
- A high-frequency, 1 μF capacitor can be added in parallel between the output and ground to filter noise and help with switching loads as data converters.

12.2 Layout Example

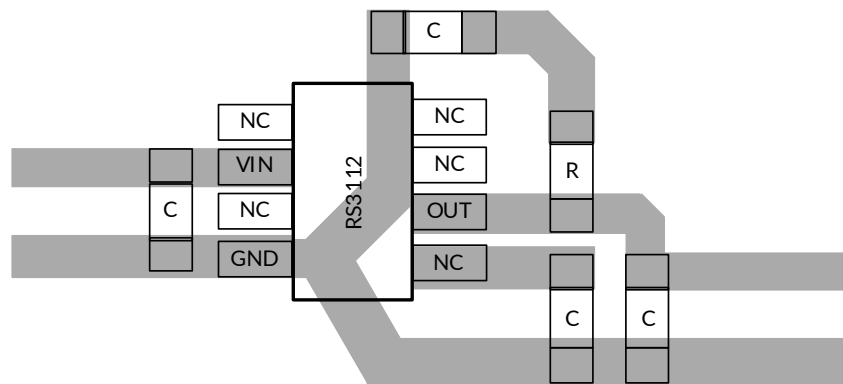


Figure 21. Layout Example

12.3 Power Dissipation

The RS3112 family is specified to deliver current loads of ± 10 mA over the specified input voltage range. The temperature of the device increases according to Equation 4:

$$T_J = T_A + P_D \times \theta_{JA}$$

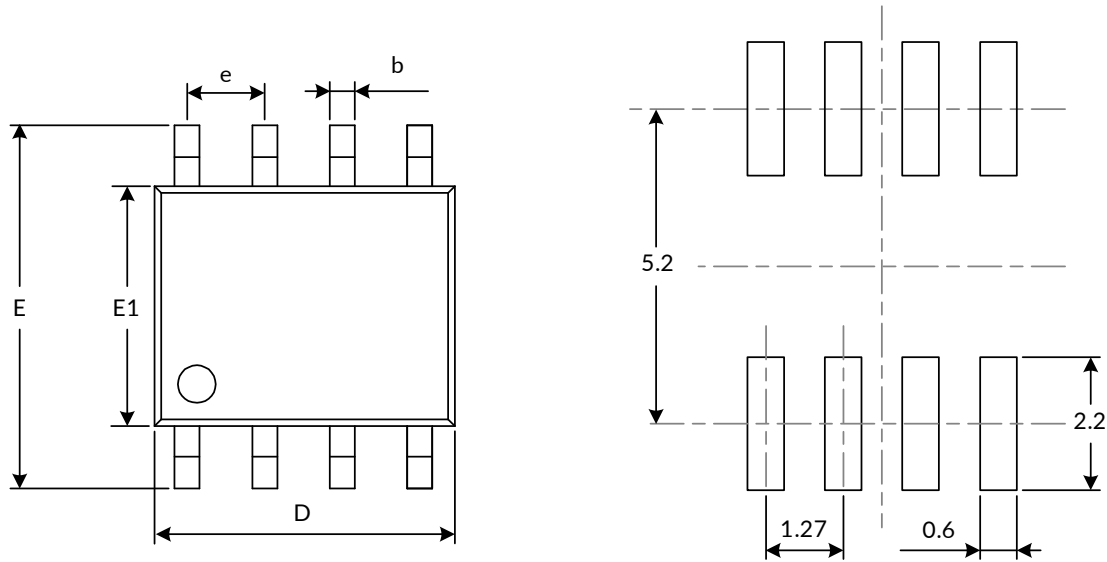
where

- T_J = Junction temperature ($^{\circ}\text{C}$)
- T_A = Ambient temperature ($^{\circ}\text{C}$)
- P_D = Power dissipated (W)
- θ_{JA} = Junction-to-ambient thermal resistance ($^{\circ}\text{C}/\text{W}$)

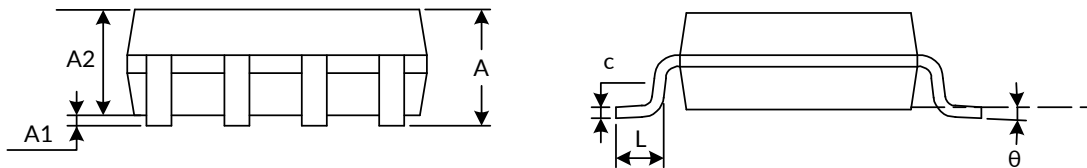
(4)

The RS3112 junction temperature must not exceed the absolute maximum rating of 150 $^{\circ}\text{C}$.

13 PACKAGE OUTLINE DIMENSIONS SOP8⁽³⁾



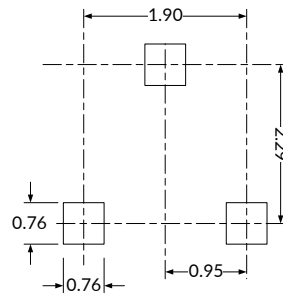
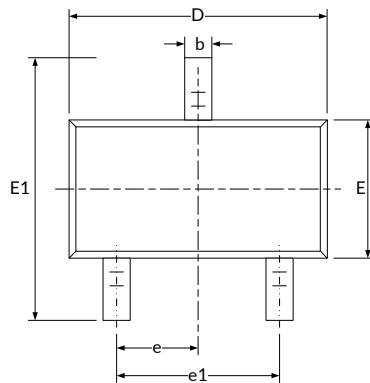
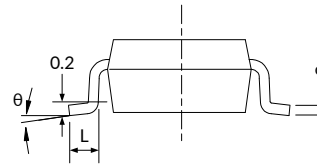
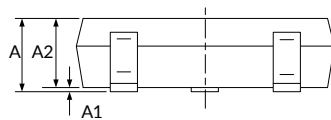
RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A ⁽¹⁾	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D ⁽¹⁾	4.800	5.000	0.189	0.197
e	1.270 (BSC) ⁽²⁾		0.050 (BSC) ⁽²⁾	
E	5.800	6.200	0.228	0.244
E1 ⁽¹⁾	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

NOTE:

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.

SOT23⁽³⁾

RECOMMENDED LAND PATTERN (Unit: mm)


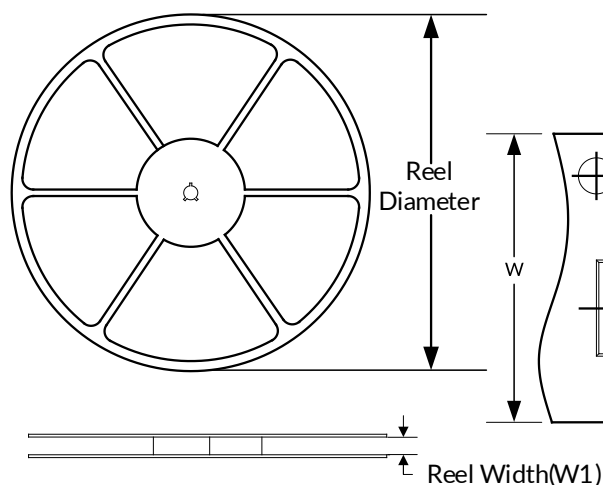
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A ⁽¹⁾	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D ⁽¹⁾	2.800	3.000	0.110	0.118
E ⁽¹⁾	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 (BSC) ⁽²⁾		0.037 (BSC) ⁽²⁾	
e1	1.800	2.000	0.071	0.079
L	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

NOTE:

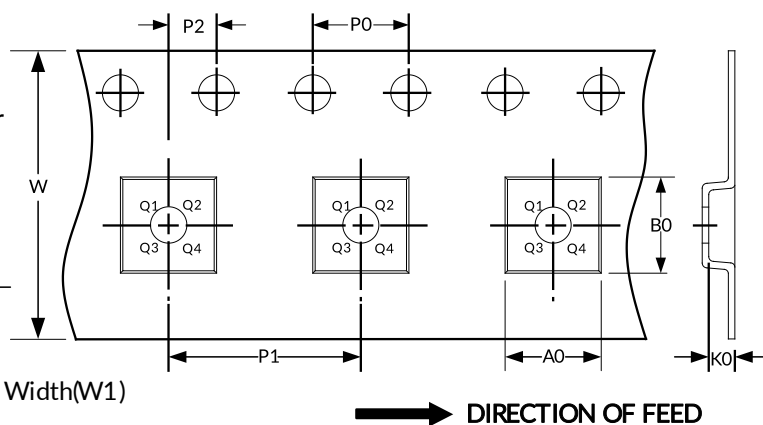
1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.

14 TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOP8	13"	12.4	6.40	5.40	2.10	4.0	8.0	2.0	12.0	Q1
SOT23	7"	9.5	3.15	2.77	1.22	4.0	4.0	2.0	8.0	Q3

NOTE:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.

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